

WHAT IS CLAIMED IS:

1. An output circuit of a semiconductor device for outputting an internal signal to an outside, comprising:

an inverter including a first transistor of a first conductivity type and a second transistor of a second conductivity type connected between  
5 lines of first and second power supply potentials and an output node, respectively, and having input electrodes both receiving said internal signal;

a third transistor of the first conductivity type and a first resistor connected in series between the line of said first power supply potential and said output node; and

10 an adjustment circuit including a fuse, for making said third transistor nonconductive when said fuse is not blown, and connecting input electrodes of said first and said third transistors when said fuse is blown, to adjust current driving capability of said output circuit.

2. The output circuit of a semiconductor device according to claim 1, further comprising a fourth transistor of the second conductivity type and a second resistor connected in series between the line of said second power supply potential and said output node,

5 wherein said adjustment circuit further makes said fourth transistor nonconductive when said fuse is not blown, and connects input electrodes of said second and said fourth transistors when said fuse is blown.

3. The output circuit of a semiconductor device according to claim 1, wherein a plurality of sets of said third transistor, said first resistor, and said adjustment circuit are provided,

5 said output circuit further comprising a monitor circuit including a fifth transistor of the first conductivity type and a sixth transistor of the second conductivity type connected between the lines of said first and said second power supply potentials and an output node, respectively, and having input electrodes both receiving a monitor signal, for determining a number of fuses to be blown.

4. The output circuit of a semiconductor device according to claim 1,  
wherein  
said semiconductor device is formed on a semiconductor substrate,  
and  
5 said adjustment circuit is provided at a corner of said semiconductor  
substrate.